

examination; (iv) to remove limitations having an effect in a foreign country which is different and unintended under U.S. practice (i.e., changing “consisting of” to “comprising”); (v) to remove or amend original claim language that could be regarded as alternative expressions that are acceptable under foreign patent practice but possibly subject to objection under U.S. practice, typically having a broadening or neutral effect in the amended claim; and/or (vi) to improve the clarity or meaning of the original language.

In the case of amendments effectively changing an original claim element expressed as a “means plus function” that could raise a presumption of claim expression under 35 U.S.C. 112, 6<sup>th</sup> paragraph to a structural expression or to an expression removing the presumption of a “means-plus-function” statement, it is not intended to narrow the claim so amended for purposes of patentability, but rather to place the claim in a form considered to be intended by the applicant from a foreign country where claim limitations described in terms of means-plus-function do not have the same effect as under U.S. practice. Thus, such amendments are intended to establish a full range of equivalents to the claim elements so amended under the U.S. doctrine of equivalents and beyond the range associated with “means-plus-function” expressions according to 35 U.S.C. 112, 6<sup>th</sup> paragraph, just as if the claim so amended was presented originally in its amended form.

All rights are reserved to the original disclosed and claimed subject matter and any cancellation of claims is made without prejudice or disclaimer.

**LIST OF CURRENT CLAIMS**

1. (Currently Amended) A smart card chip, comprising ~~with~~ a nonvolatile system memory (~~ROM, flash1~~), a Java Card Virtual Machine implemented in the nonvolatile system memory (~~ROM, flash1~~), a nonvolatile application memory (~~EEPROM, flash2~~), a volatile working memory (~~RAM~~) and a variables memory area reserved for global variables, the variables memory area being reserved in the volatile working memory (~~RAM~~).
2. (Currently Amended) The smart card chip according to claim 1, wherein the variables memory area is accessible ~~can be accessed~~ only by programs stored in the system memory (~~ROM, flash~~).
3. (Currently Amended) The smart card chip according to claim 1 ~~or 2~~, wherein the variables memory area is accessible ~~can be accessed~~ only by programs that have been implemented in the smart card chip up to the end of completion of the smart card chip.
4. (Currently Amended) The smart card chip according to claim 1 ~~any of claims 1 to 3~~, wherein the variables memory area is reserved statically.
5. (Currently Amended) The smart card chip according to claim 1 ~~any of claims 1 to 4~~, wherein the variables memory area is reserved by access information directed directly to the working memory (~~RAM~~).
6. (Currently Amended) The smart card chip according to claim 2 ~~any of claims 1 to 5~~, wherein said ~~these~~ programs may ~~can~~ access the variables memory area that can link to the variables memory area.

7. (Currently Amended) The smart card chip according to claim 6, including a ~~wherein the ability of a program to link to the variables memory area is obtained by~~ the program having available for linking an export component on the smart card chip to enable the program to link to the variables memory.

8. (Currently Amended) The smart card chip according to claim 2 ~~any of claims 1 to 7~~, wherein said ~~these~~ programs are excluded from using the variables memory area that cannot link to the variables memory area.

9. (Currently Amended) The smart card chip according to claim 8, including a ~~wherein the inability of a program to link to the variables memory area is obtained by~~ the program having withheld therefrom for linking an export component of the smart card chip to prevent the program to link to the variables memory area.

10. (Currently Amended) The smart card chip according to claim 1 ~~any of claims 1 to 9~~, wherein the variables memory area is reserved by a Java package (~~RAM package~~) implemented in the smart card chip.

11. (Currently Amended) The smart card chip according to claim 10, wherein the Java package (~~RAM package~~) is implemented in the system memory (~~ROM, flash~~).

12. (Currently Amended) The smart card chip according to claim 10 ~~or 11~~, wherein the Java package contains exclusively the reservation of the variables memory area.

13. (Currently Amended) The smart card chip according to claim 10 ~~any of claims 10 to 12~~, wherein an export component of the Java package (~~RAM package~~) containing the link information required for linking to the reserved variables memory area is not implemented in the smart card chip.

14. (Currently Amended) The smart card chip according to claim 1 ~~any of claims 1 to 13~~, wherein the Java Card Virtual Machine is so formed, and modified when required, that it enables ~~permits~~ the use of global variables in the volatile working memory (~~RAM~~) on the basis of the variables memory area reserved in RAM.

15. (Currently Amended) The smart card chip according to claim 14, wherein the Java Card Virtual Machine is modified such that the modifications are not qualitatively recognizable externally, ~~in particular that the smart card chip meets a predetermined Java Card specification that the chip would likewise meet without the modifications.~~

16. (Currently Amended) A chip module having a smart card chip according to claim 1 ~~any of claims 1 to 15~~.

17. (Currently Amended) A data carrier, ~~in particular Java Card~~, having at least one ~~of~~ a smart card chip according to claim 1 and ~~any of claims 1 to 15 and/or~~ a chip module according to claim 16.

18. (Currently Amended) A method for reserving a variables memory area in a smart card chip having ~~[[:]]~~ a nonvolatile system memory (~~ROM, flash1~~), a Java Card Virtual Machine implemented in the nonvolatile system memory (~~ROM, flash1~~), a nonvolatile application memory (~~EEPROM, flash2~~) and a volatile working memory (~~RAM~~), wherein the method comprising ~~involves~~ implementing a Java program code in the smart card chip by which a variables memory area for global variables is reserved in the volatile working memory (~~RAM~~).

19. (Currently Amended) The method according to claim 18, wherein the Java program code implemented is a Java package (~~RAM package~~).

International Application No. PCT/EP2004/013797  
Attorney Docket: VOLL3001/JEK

20. (Original) The method according to claim 19, wherein an export component of the Java package containing the link information required for linking to the variables memory area is not implemented in the smart card chip.

21. (Currently Amended) The method according to claim 18 ~~any of claims 18 to 20~~, wherein an export file required for linking to the reserved variables memory area is made available only to those programs for linking that may ~~are to be able to~~ access the variables memory area.